

AUTHOR: RICCARDO MUNARO
WCAID:2014MUNA01

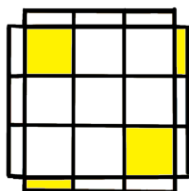
REDUCED CFCE/PLATFORM METHOD FOR LL(LAST LAYER)

After Cross and F2L:

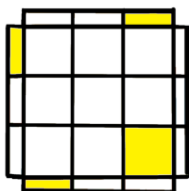
- 1-Oriented Corners Last layer.
- 2-Permutation Corners Last Layer.
- 3-Creation Block 1x1x3.
- 4-Solve Cases Line.

Oriented Corners Last Layer:

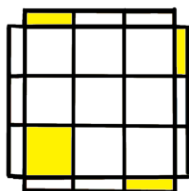
F R U' R' U' R U R' F'



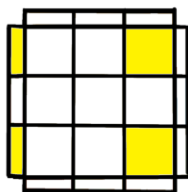
L' U R U' L U R'



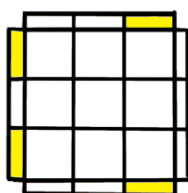
R U' L' U R' U' L



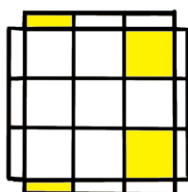
F R U R' U' F'



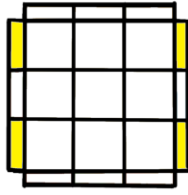
FRUR'U'RUR'U'F'



RUR'U'R'FRF'

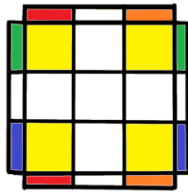


RUR'URU'R'URU²R'

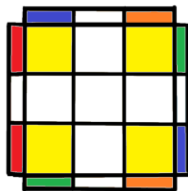


Permutation Corners Last Layer:

$F R U' R' U' R U R' F' R U R' U' R' F R F'$

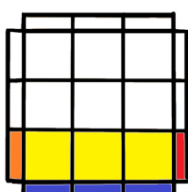
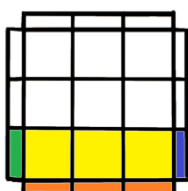
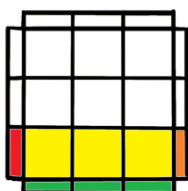
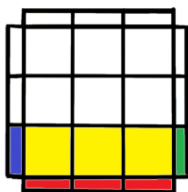


$R U^2 R' U' R U^2 L' U' R' U' L$

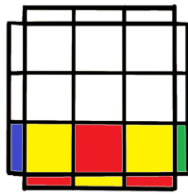


Creation Block 1x1x3:

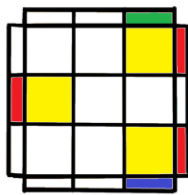
Block 1x1x3 can have 4 colours:



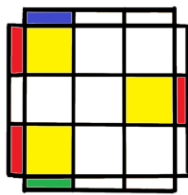
M' U M' U M' U² M U M U M



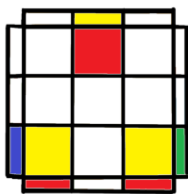
M' U' M U² M' U' M



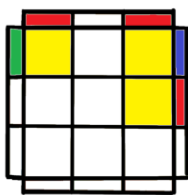
M' U M U² M' U' M



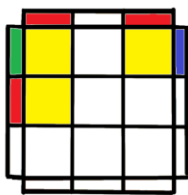
R U R' U' M' U R U' R_w



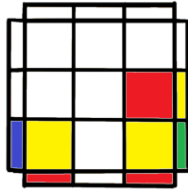
R U R' U' M' U R U' Rw



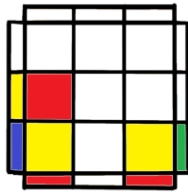
L' U' L U M' U' L' U Lw



R' F' L F R L' U' L' U L

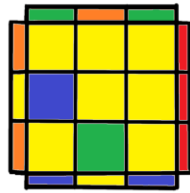


L F R' F' L' R U R U' R'

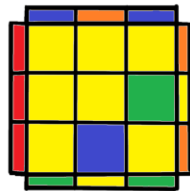


Solve Cases Line:

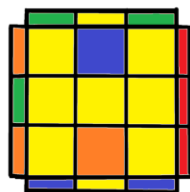
R' F' L F R L' U' L' U L



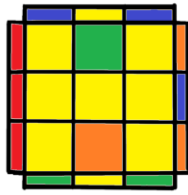
L F R' F' L' R U R U' R'



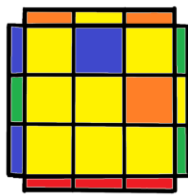
L' U' L U M' U' L' U Lw



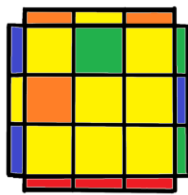
R U R' U' M' U R U' Rw



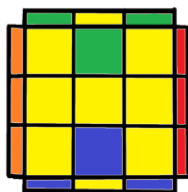
M' U' M U² M' U' M



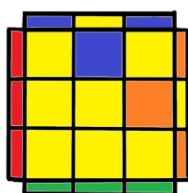
M' U M U² M' U' M



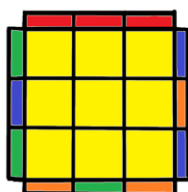
M' U M' U M' U² M U M U M



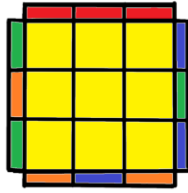
R U' R' U' M' U M' U M' U² M' U M U M U' R U R'



R² U R U R' U' R' U' R' U R'



L² U' L' U' L U L U L U' L



Conclusion:

This a version Reduced to Platform Standard.
In Platform Standard I created a guide in step one
with 50 cases and 25 algs (1 alg can solve 4 cases
block 1x1x3) for solve block 1x1x3 and in second
step solve 71 cases Line with 71 algs.

It's call this still Reduced CFCE.

CFCE solve Cross + F2L + CMLL (Corner Last
Layer) + ELL (Edge Last Layer).

In this version Reduced I use in final Step 8
ELL/Line and 2 PLL/Line.